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Basics of OS

(Computer System

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ARCHITECTURE?

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development of  
TIFRAC, the first  
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1955. After its  
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1959, he was  
involved with  
computer research  
activities at the  
University of Illinois  
and took part in the  
development of  
ILLIAC II, which was

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commissioned in  
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Paranandi Venkata  
Suryanarayana Rao is  
an Indian computer  
scientist, known for  
his researches in the  
fields of speech and

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System recognition

and is credited with contributions for the development of

TIFRAC, the first

indigenously

developed electronic

computer in India. He

is a recipient of

awards such as IEEE

Third Millenium

Medal, Vikram

Sarabhai Award, Om

Prakash Bhasin

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System and VASVIK  
Industrial Research  
Award.

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book introduces a  
main concepts about  
computer system  
architectures , gives a  
a brief ideas about  
computer systems  
structures , memory

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Bas Pys

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method based on  
geometrical tools to  
map problems onto

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regular and  
synchronous  
processor arrays. The  
problems we  
consider are defined  
by systems of affine  
recurrence equations  
(SARE). From such a  
problem specification  
we extract the data  
dependencies in  
terms of two classes  
of vectors: the  
utilization vectors



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and the dependence  
vectors.

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~~Geometrical tools to  
map systems of affine  
recurrence ...~~

Rao, A. S., Georgeff,  
M. P. (1993) A model-  
theoretic approach to  
the verification of  
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Proceedings of the  
Thirteenth

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International Joint  
Conference on  
Artificial Intelligence  
(IJCAI-93),  
Chamberey, France.

Rational Software  
Agents: From Theory  
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Recent years have  
seen a shift in  
perception of the  
nature of HCI and  
interactive systems.

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As interface work has increasingly become a focus of attention for the social sciences, we have expanded our appreciation of the importance of issues such as work practice, adaptation, and evolution in interactive systems.

~~Developing a~~

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~~reflective model of  
collaborative systems~~

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system for projective

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System that parallel  
lines meet at ...

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Laboratory,

University of

Cambridge, UK, April,

1992. Will also appear

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Society Press: Los

Alamitos, CA. To be

published in 1993.

I.A. Ferguson. Toward

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~~TouringMachines: an~~

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This paper presents a novel virtual-channel (VC) sharing technique for NoC architecture. The proposed architecture improves the utilization of resources to enhance the performance with minimal overheads. A



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heuristic approach towards the proper VC sharing strategy is proposed, which is performed by an adaptive algorithm that configures the VC sharing based on link load parameters.

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