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Lessons Q. 5.10: A sequential circuit has two JK flip-flops A and B, two inputs x and y, and one output z Q.

4.14: Assume that the exclusive-OR gate has a propagation delay of 10 ns and that the AND or OR ~~InDesign~~

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following function as sum of minterms and product of maxterms: $F = B'D + A'D + BD$

~~Q. 2.22: Convert the~~

~~following expressions into SOP, POS:~~

~~$(u + xw)(x + uv), x + x(x + y)(y + z)$~~

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level description of the priority encoder circuit shown in Fig. 4.23.

EX 1 ||DIGITAL SIGNAL || Particular Solution of First Order Difference Equation : $y(n) + ay(n-1) = x(n)$ Q. 5.1: The D latch of Fig. 5.6 is constructed with four NAND gates and an inverter. Consider the ~~Digital Design Fourth~~

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1.4 14-bit binary: 11_1111_1111_

Decimal: $2^{14} - 1 = 16,383$ 10

Hexadecimal: 3FFF 16. 1.5 Let $b =$

base. (a) $14/2 = (b + 4)/2 = 5$, so $b = 6$.

(b) $54/4 = (5*b + 4)/4 = b + 3$, so $5 * b$

$= 52 \div 4$, and $b = 8$. (c) $(2 * b + 4) + (b +$

$7) = 4b$, so $b = 11$. 1.6 $(x \div 3) (x \div 6) =$

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$$x^2 - (6 + 3)x + 6 \cdot 3 = x^2 - 11x + 22.$$

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$$1.6. (x + 3)(x + 6) = x^2 + (6 + 3)x + 6 \cdot 3$$

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= $x^2 - 11x + 22$ Therefore: $6 + 3 = b + 1m$, so $b = 8$ Also, $6 * 3 = (18)_{10} = (22)_8$. 1.7. $64_{CD16} = 0110_0100_1100_1101_2 = 110_010_011_001_101 = (62315 \dots$

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